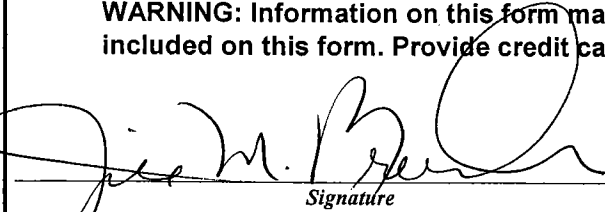



AMENDMENT TRANSMITTAL LETTER (Large Entity)					Docket No. POU920030173US1	
Applicant(s): Rich et al.						
Application No. 10/753,852	Filing Date 01/08/2004	Examiner Vuthe Siek	Customer No. 46369	Group Art Unit 2825	Confirmation No. 6046	
Invention: SCALABLE LOGIC SELF-TEST CONFIGURATION FOR MULTIPLE CHIPS						
<u>COMMISSIONER FOR PATENTS:</u>						
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.						
CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE	
TOTAL CLAIMS	20 -	24 =	0	x \$50.00	\$0.00	
INDEP. CLAIMS	4 -	4 =	0	x \$200.00	\$0.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00	
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0463 (:IBM) <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17. <input type="checkbox"/> Payment by credit card. Form PTO-2038.						
WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.						
 _____ Signature			Dated: August 4, 2006			
Jill M. Breedlove, Esq. Reg. No. 32,684 Heslin Rothenberg Farley & Mesiti P.C. 5 Columbia Circle Albany, NY 12203 Telephone: (518) 452-5600 Facsimile: (518) 452-5579			I hereby certify that this correspondence is being transmitted electronically to: Examiner Vuthe Siek, Group Art Unit 2825, United States Patent and Trademark Office, Alexandria, VA 22313-1450, on August 4, 2006.  _____ Jill K. Becker			
CC:						

AMENDMENT TRANSMITTAL LETTER (Large Entity)Applicant(s): **Rich et al.**

Docket No.

POU920030173US1

Application No.

10/753,852

Filing Date

01/08/2004

Examiner

Vuthe Siek

Customer No.

46369

Group Art Unit

2825

Confirmation No.

6046Invention: **SCALABLE LOGIC SELF-TEST CONFIGURATION FOR MULTIPLE CHIPS****COMMISSIONER FOR PATENTS:**

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TOTAL CLAIMS	20 -	24 =	0	x \$50.00	\$0.00
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Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
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Signature**Jill M. Breedlove, Esq.**

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Dated: **August 4, 2006**

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United States Patent and Trademark Office,
Alexandria, VA 22313-1450, on August 4, 2006.


Jill K. Becker

cc: